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Dwyatt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of: )  
S. Halepete et al )  
Serial No. 09/484,516 )  
Filed: 1/18/00 )  
For: ADAPTIVE POWER CONTROL )  
Examiner: P. Myers  
Art Unit 2181

Preliminary Amendment

**RECEIVED**

MAY 13 2002

Technology Center 2100

Dear Sir:

In response to the Office Action mailed November 7, 2001, please amend the above-identified patent application as follows:

In the claims:

Cancel Claims 4, 5, and 7.

Please substitute the following claims for pending claims of the same number:

1 Claim 1 (twice amended). A method for controlling the operating condition of a computer processor on a chip including a clock generator on the chip capable of providing a plurality of clock frequencies which can be individually selected concurrently comprising the steps of:

5 determining a maximum allowable power consumption level from the 6 operating condition of the processor,